

⑮ VPPPO TECHNOLOGY REQUIRED - ALL VIAS TO BE NON-CONDUCTIVE EPOXY FILLED AND PLATED OVER.



Slot definitions : Routed Path Length = Calculated from tool start centre position to tool end centre position.  
Hole Length = Routed Path Length + Tool Size = Slot length as defined in the PCB layout

Impedance requirements: PCB trace widths and spacings

	100 ohm diff	100 (Trace Spacing)	90 ohm diff	90 (Trace Spacing)	50ohm
Layer 1	5.1	6	7	6	5.1
Layer 2 (Gnd Plane)					
Layer 3	4	6.2	4.7	5	4
Layer 4	4	6.2	4.7	5	4
Layer 5 (Power Plane)					
Layer 6 (Power Plane)					
Layer 7	4	6.2	4.7	5	4
Layer 8	4	6.2	4.7	5	4
Layer 9 (Gnd Plane)					
Layer 10	5.1	6	7	6	5.1